

Electronic Acknowledgement Receipt

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International Application Number:	
Confirmation Number:	9608
Title of Invention:	Method and apparatus for defect analysis of semiconductor integrated circuit
First Named Inventor/Applicant Name:	Masahiro Ishida
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment After Final	transmittal.pdf	36578 <small>etadfeb387d7f4852a99200ba7dcb4411b40536</small>	no	2

Warnings:

Information:

2	Claims	claims.pdf	117994 16dd1276Dd6015d1a2622a1d178c35e5c d1ee9	no	4
Warnings:					
Information:					
3	Applicant Arguments/Remarks Made in an Amendment	remarks.pdf	152446 6914722768-b0713d2a9f80b6996a76c51d 4861	no	6
Warnings:					
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Total Files Size (in bytes):			307018		
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